

XA-9625

PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasutoshi AIBARA et al.

Appln. No.: 10/080,586

Group Art Unit: 2878

Filed: February 25, 2002

Examiner: S. Allen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
IMAGING SYSTEM

Allowed: December 2, 2003

Confirmation No.: 8708

\* \* \*

PETITION UNDER 37 C.F.R. § 1.182 TO ADD TO THE  
LETTERS PATENT (NOT YET PRINTED) A THIRD ASSIGNEE  
OMITTED FROM THE ISSUE FEE TRANSMITTAL- Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450Attn: Mail Stop Petition

Sir:

Applicants respectfully request that a third Assignee,  
namely, Hitachi Tohbu Semiconductor, Ltd., be added to the  
Letters Patent before it is printed.

Through a clerical error, only two Assignees were  
named on the Issue Fee Transmittal when the Issue fee was

paid on January 27, 2004. Three Assignees are named in the Assignment recorded on February 25, 2002 at Reel 012632/Frame 0483.

A corrected Issue Fee Transmittal is attached to this Petition.

An accompanying Petition requests expedited consideration of the present Petition in order, hopefully, to add the third Assignee before the printing of the Letters Patent.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees that may be required by this paper and to credit any overpayment to that Account.

Respectfully submitted,

By: Nelson H. Shapiro  
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Reg. No. 17,095

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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on March 31, 2004.

Nelson H. Shapiro  
Nelson H. Shapiro